

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

V - SEMESTER(w.e.f. Batch 2018-19)

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Assessment*	End Sem University Exam	Teachers Assessment*		
1	BTEC501	Microprocessor, Microcontroller and Interfacing		3	1	2	60	20	20	30	20	150	5
2	BTEC502	Cellular and Mobile Communication		3	0	0	60	20	20	0	0	100	3
3	BTEC503	Digital Communication		3	0	2	60	20	20	30	20	150	4
4	BTEC504	CMOS VLSI Design		3	1	2	60	20	20	30	20	150	5
5	BTEC515/ BTEC525/ BTEI611	Electives: 1 Data Communication and Computer Networks 2 FPGA Based System Design 3 Data Acquisition System		3	1	2	60	20	20	30	20	150	5
6	BTEC506	Minor Project		0	0	2	0	0	0	30	20	50	1
7	BTEC507	Programming in Python		0	0	2	0	0	0	30	20	50	1
TOTAL				15	3	12	300	100	100	180	120	800	24

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students are advised to take online MOOCS course of minimum two credits

A.C. Chandra

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, INDORE

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Controller of Examinations
Shri Vaishnav Vidyapeeth Vishwavidyalaya

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Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

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Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

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Chairperson
Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore